`include "transaction.sv"

`include "generator.sv"

`include "driver.sv"

`include "interface.sv"

`include "monitor.sv"

`include "scoreboard.sv"

`include "environment.sv"

`include "test.sv"

module top();

bit clk,rst;

test tb;

intf pif(clk,rst);

vending\_machine dut(.sel(pif.sel),.clk(pif.clk),.rst(pif.rst),.coin(pif.coin),.item(pif.item),.change(pif.change));

always #5 clk=~clk;//by default clk=0;

initial

begin

clk=1;

rst=1;

#3 rst=0;

end

initial begin

$display("display the top");

tb=new(pif);

tb.main();

end

initial

begin

$dumpfile("dump.vcd");

$dumpvars();

#500;

$finish();

end

endmodule